

Listing of the Claims

1. (currently amended) A pod for transporting a cassette of semiconductor wafers, comprising:

a body member constructed of a top wall, a bottom wall, a front wall and two sidewalls forming a back opening for loading or unloading wafers into or out of said body member;

a cover member for sealingly and removably engaging said back opening of the body member; and

latch means carried on said cover member for latching said cover member onto said body member, said latch means being actuatable and operable linearly from a latched condition in which said cover member is latched onto said body member to a released condition allowing removal of said cover member from said body member when engaged linearly by a latch key of a door opener situated in a loadport onto which said pod is positioned, said latch means further comprises two latch holes for linearly engaging said latch key of the door opener in said loadport.

2. (original) A pod for transporting a cassette of semiconductor wafers according to claim 1, wherein said latch means further comprises at least two locking tabs for engaging receptacles provided in said body member.

3. (original) A pod for transporting a cassette of semiconductor wafers according to claim 2, wherein said at least two locking tabs being operated in a linear manner when engaging or disengaging said receptacles.

4. (cancelled)

Q2 5. (currently amended) A pod for transporting a cassette of semiconductor wafers according to claim ~~4~~ 1, wherein each one of said two latch holes operates at least one locking tab for engaging one of two opposite sides of a loadport opening.

6. (currently amended) A pod for transporting a cassette of semiconductor wafers according to claim ~~4~~ 1, wherein said two latch holes move in a linear manner to linearly engage at least two locking tabs of said latch means to an opening of said loadport.

7. (original) A pod for transporting a cassette of semiconductor wafers according to claim 2, wherein said at least two locking tabs are spring-loaded for returning said at least two locking tabs to a disengaged position.

8. (currently amended) A pod for transporting a cassette of semiconductor wafers, comprising:

a body member constructed of a top wall, a bottom wall, a front wall and two sidewalls forming a back opening for loading or unloading wafers into or out of said body member;

a cover member for sealingly and removably engaging said back opening of the body member; and

Q2 latch means carried on said cover member for latching said cover member onto said body member, said latch means further comprises:

~~at least two~~ four locking tabs with ~~at least one operating two~~ situated on one of two opposing sides of said cover member for engaging receptacles provided in said body member; and

at least two latch holes with one ~~connected to~~ latch hole being part of and linearly operates said at least one locking tab when the latch hole is engaged by a latch key of a door opener in the loadport.

9. (cancelled)

10. (original) A pod for transporting a cassette of semiconductor wafers according to claim 8, wherein said at least two locking tabs are spring-loaded.

N^h
ar 11. (original) A pod for transporting a cassette of semiconductor wafers according to claim 8 further comprising an indicator means including means responsive to the position of said at least two latch holes to produce a signal related to the condition of said latch means.

12. (original) A pod for transporting a cassette of semiconductor wafers according to claim 8, wherein said latch means further comprises a drive plate operably connected to said at least two locking tabs such that said at least two locking tabs are drivable by said drive plate to shift said latch means between a latching and an unlatching condition.

13. (new) A pod for transporting a cassette of semiconductor wafers, comprising:

a body member constructed of a top wall, a bottom wall, a front wall and two sidewalls forming a back opening for loading or unloading wafers into or out of said body member;

a cover member for sealingly and removably engaging said back opening of the body member; and

Q2 latch means carried on said cover member for latching said cover member onto said body member, said latch means being actuatable and operable linearly from a latched condition in which said cover member is latched onto said body member to a released condition allowing removal of said cover member from said body member when engaged linearly by a latch key of a door opener situated in a loadport onto which said pod is positioned, said latch means further comprises at least two spring-loaded locking tabs for engaging receptacles provided in said body member and for returning said at least two spring-loaded locking tabs to a disengaged position.

14. (new) A pod for transporting a cassette of semiconductor wafers according to claim 13, wherein said at least two locking tabs being operated in a linear manner when engaging or disengaging said receptacles.

15. (new) A pod for transporting a cassette of semiconductor wafers according to claim 13, wherein said latch means further comprises two latch holes for linearly engaging said latch key of the door opener in said loadport.

Q2 16. (new) A pod for transporting a cassette of semiconductor wafers according to claim 15, wherein each one of said two latch holes operates at least one locking tab for engaging one of two opposite sides of a loadport opening.

17. (new) A pod for transporting a cassette of semiconductor wafers according to claim 15, wherein said two latch holes move in a linear manner to linearly engage at least two locking tabs of said latch means to an opening of said loadport.
